AMENDMENTS TO THE SPECIFICATION:

Please replace paragraph [0039] with the following amended paragraph:

[0001] If the data signal with a logic value '0' is supplied to the address electrode line X, the data pulse Vd having a minute width [[Td]] Tb smaller than the width [[Td]] Ta of the data pulse with a logic value '1', for an example, in a range of 0.8 μs, is generated. If the data signal with a logic value '1' are supplied to the address electrode lines X continuously, a data pulse Vd with a pulse width Tc in a range of 2.5μs is applied to the address electrode line X. That is, the pulse width Tc of the data pulse Vd when the data signal with a logic value '1' is supplied to the address electrode line X has a pulse width of two times of a pulse width 1.4μs of the data pulse Vd with a logic value '1' minus the overlapped time period 0.3μs of the scanning pulses Vs.